PLC200 SLC-500 Timer Instructions

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On-Delay Timer Review



On Delay Timers:

- The time cycle starts when the timer coil is turned on.
- When LS-1 closes, TR-1 coil is energized and starts the time cycle.
- Then the TR-1 relay contact in line 2 closes turning on SOL-1.
- After the time delay, the delay contact in line 3 closes and energizes the pilot light. Both outputs remain on.
- When LS-1 opens, TR-1 deenergizes and resets the circuit shutting off both outputs.

Off-Delay Timer Review



Off Delay Timers:

- The time cycle starts when the timer coil is turned off.
- When LS-1 closes, TR-1 coil is energized.
- Immediately the TR-1 relay contact in line 2 closes turning on SOL-1, and the TR-1 delay contact in line 3 closes turning on PL-1.
- When LS-1 opens, TR-1 coil goes off, and the TR-1 relay contact opens shutting off SOL-1. TR-1 delay contact remains closed, and the time cycle start.
- After the time cycle is complete, the delay contact in line 3 opens, shutting off PL-1, and the circuit is reset.

Data in an SLC-500 Timer Instruction



Timer Address: Each timer must have it's own unique address.

Time Base: Determines the duration of each timing interval. The setting can be 1 sec., 0.01 sec., or 0.001 sec.

Timer Preset: This is the value the timer must time up to, in order to fulfill the time cycle.

Timer Accumulated: This value will show the time currently accumulated or elapsed.

The **timed value** will be the Time Base times the Timer Preset. If the preset value is 1500, and the time base is 0.01 sec., the timer would be a 15 second timer.

Timer Status Bits





Timer status bits are bits in the timer data file that are set and resets according to the operation of the timer. Instructions in the ladder program(s) are referenced from these status bits, and will be used to control logic in the program.

The three status bits are: **.EN** – The enable bit.

.TT – The timer timing bit.

.DN – The timer done bit.

TON Instruction



- TON stands for Timer On Delay This is a non-retentive timer.
- When the TON is powered, it starts timing. When it is shut off, the timer accumulated value and status bits are reset.
- TON Status Bits:
 - EN Timer Enable bit. This bit is on (1) when the TON instruction is powered.
 - TT Timer Timing bit. This bit is on (1) when the TON is timing
 - DN Timer Done bit. This bit is on (1) when the TON is timed out (Accum value is equal to the Preset value).

Example TON Program



Example TON Program 2



TON used in hold-in logic



The TON is used in this program to reset the circuit after the time delay.

The CYCLE_START_PB input is pressed, which will complete the logic power flow to the TON. The timer starts timing, the Enable bit comes on which is in parallel with the CYCLE_START_PB XIC. The CUTTER_SOL comes on right away as well.

After the timer times out (8.5 seconds), the T4:2/DN bit comes on, which will make the XIO instruction false, which will open the power going to the timer, resets it, and the circuit is reset.

RTO & RES Instruction



- RTO stands for Retentive Timer On Delay This is a retentive timer, which means when it is shut off, the accum value is not reset.
- When the RTO is powered, it starts timing. When it is shut off, the timer accumulated value stays where it is at, and starts from there when it is turned back on.
- The RES Reset instruction, resets the accumulated value and status bits to zero when it is energized.
- RTO Status Bits:
 - .EN Timer Enable bit. This bit is on (1) when the RTO instruction is powered.
 - .TT Timer Timing bit. This bit is on (1) when the RTO is timing
 - .DN Timer Done bit. This bit is on (1) when the RTO is timed out (Accum value is equal to the Preset value).

Example RTO Program



Example RTO Program continued



Example RTO Program continued



Example RTO Program continued



TOF Instruction



- TOF stands for Timer Off Delay This is an off delay timer.
- When the process goes to the Run Mode, the Accum value is adjusted to equal the Preset value. The DN bit is off at this time.
- When the TOF is powered, the Accum value goes to zero and the DN bit comes on. When the TOF shuts off, the TOF starts timing, then when it times out, the DN bit shuts off.
- TOF Status Bits:
 - EN Timer Enable bit. This bit is on (1) when the TOF instruction is powered.
 - TT Timer Timing bit. This bit is on (1) when the TOF is timing
 - DN Timer Done bit. This bit is on (1) when the Accum value is <u>not</u> equal to the Preset.

Example of a TOF Instruction



TOF Instruction Continued



TOF Instruction Continued



Timer values stored in the Data Files and displayed in the Ladder Files

Data Files store discrete and numeric values in address locations (sometimes called registers) that can be accessed by instructions in the program or from external hardware devices such as HMIs.



Changing the Timer preset value on or offline



Changing the Timer preset value from the data file



Timer Addressing

- A timer element is addressed as T4:10.
- A timer element is made up of 3 words:
 - Preset: T4:10.PRE
 - Accum: T4:10.ACC
 - Status bits: T4:10/EN, T4:10/TT, T4:10/DN
- Each word consists of 16 bits in the preset and accum words.
- Only 3 bits are accessible in the status word
- A user could see bits addressed as: T4:10.ACC/2 (the bit is 0-15)

Timer Data Files

- Data File #4 is a timer file by default.
- Each timer file can have 256 timers (0-255)
- Data files 0-8 are predefined, and files 9-255 can be defined to any type of file except: input, output or status file types.
- In this example, a T9 and T10 data files were added. When these files are opened up, they view the data in a timer format.





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