

PLC220 Lab 1: Demo-Board Configuration

Required Materials

- ControlLogix Demo Board with 1756-L71 processor and Ethernet Commuication Module
- Computer with Studio 5000 and RSLinx software
- Ethernet TP cable(s)
- Lesson File Module 1 Lesson 2.0 Getting Started Studio 5000
- Lesson File Module 1 Command Line Utilities
- 1. Using IPConfig utility dertermine:

IP Address of Computer:_____ Subnet Mask of Computer:_____ Physical (MAC) Address of Computer:

2. Using RSLinx software – connect the computer to the Ethernet module on the ControlLogix Demo Board using TP cables

Verify Connection using ETHIP driver in RSLinx RSWho window

Match the IP Address scrolling across the 1756 Ethernet module's

display to the IP Address shown in RSLinx ETHIP driver.

192.168.101.59 shown in Figure 1-A

Expand the IP driver to view modules in demo board chassis

AB_ETHIP-6, Ethernet
 I92.168.101.59, 1756-EN2TR, 1756-EN2TR/C 217021900
 ■ Backplane, 1756-A10/B
 ■ 00, 1756-L71 LOGIX5571, local
 ■ 01, 1756-EN2TR, 1756-EN2TR/C 217021900
 02, 1756-IB16/A, 1756-IB16/A DCIN
 03, 1756-OB16E/A, 1756-OB16E/A DCOUT EFUSE
 04, 1756-IF8/A, 1756-IF8/A
 05, 1756-OF4/A, 1756-OF4/A
 ■ 06, 1756-DNB, 1756-DNB DeviceNet Scanner



Figure 1 - A

RSLinx – ETHIP Driver

- 3. Use PING utility to verify connection between computer and ControlLogix Ethernet Module
- 4. From RSLinx determine the following information of the demo board hardware.

Chassis Part Number:				
Slot Number	Module Part #	Module Revision #		

5. Using Module 1 Lesson 2.0 – Getting Started Studio 5000. Configure an I/O Configuration for the demo board hardware. Write a 1 Rung Project file is that SS7 Switch will turn ON PL 3 indicator light. Add documentation as shown in Figure 2 – A.

Note: Ask Instructor for assistance if there is no Ethernet connection Configuring a 1756 Ethernet Module is covered in Module 2.



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0	SS7 Local:2:LData.7	Loc	PL3 at3:0.Data.3
(End)	L s		=



- Save the Project File as Module1_Lab1.ACD
 Save the Project File as Module1_Lab1.L5K
- 7. Download the Project File to the 1756-L71 processor on the demo board.
- 8. Verify Project file operation

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